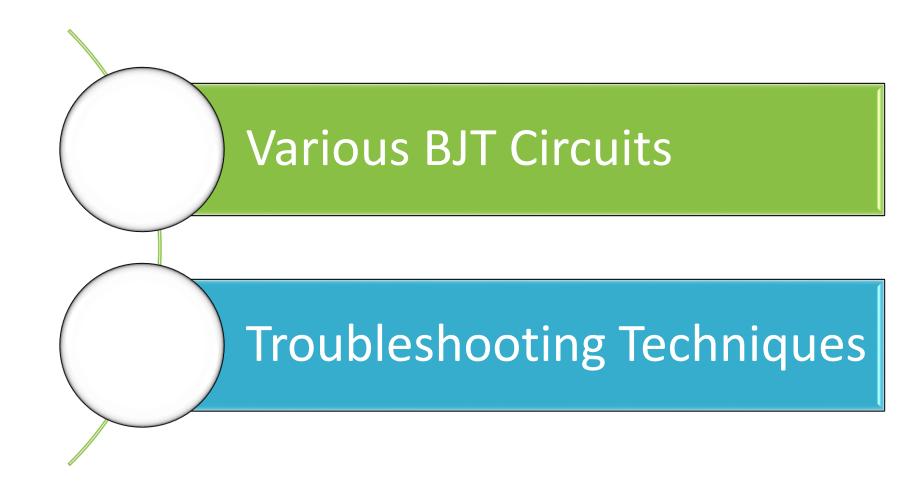
# ECE 312 Electronic Circuits (A)

Lec. 4: BJT Circuits & Troubleshooting

Instructor

Dr. Maher Abdelrasoul

### Outline

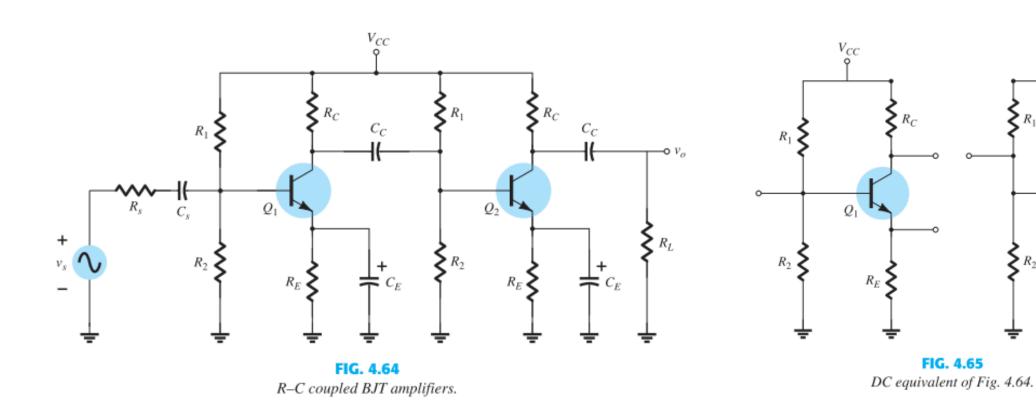


## Various BJT Circuits

- MULTIPLE BJT NETWORKS
- CURRENT MIRRORS
- CURRENT SOURCE CIRCUITS
  - Bipolar Transistor Constant-Current Source
  - Transistor/Zener Constant-Current Source
- PNP TRANSISTORS
- TRANSISTOR SWITCHING NETWORKS

# Multiple BJT Networks (1 of 6)

• R–C coupling



 $V_{CC}$ 

### Multiple BJT Networks (2 of 6)

Darlington configuration

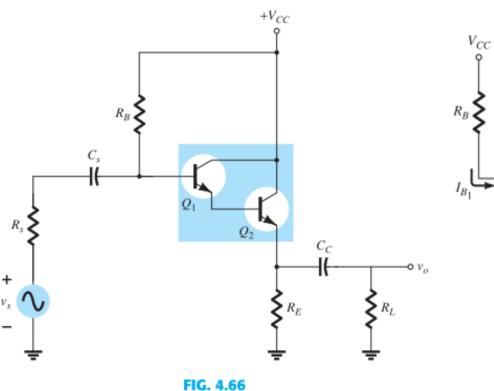


FIG. 4.66

Darlington amplifier.

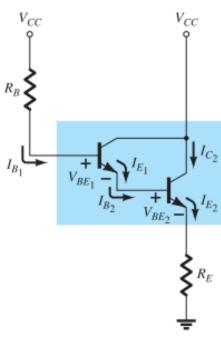


FIG. 4.67
DC equivalent of Fig. 4.66.

$$I_{B_1} = \frac{V_{CC} - V_{BE_1} - V_{BE_2}}{R_B + (\beta_D + 1)R_E}$$

$$V_{BE_D} = V_{BE_1} + V_{BE_2}$$

$$I_{B_1} = \frac{V_{CC} - V_{BE_D}}{R_B + (\beta_D + 1)R_E}$$

$$I_{C_2} \cong I_{E_2} = \beta_D I_{B_1}$$

$$\beta_D = \beta_1 \beta_2$$

$$V_{E_2} = I_{E_2} R_E$$

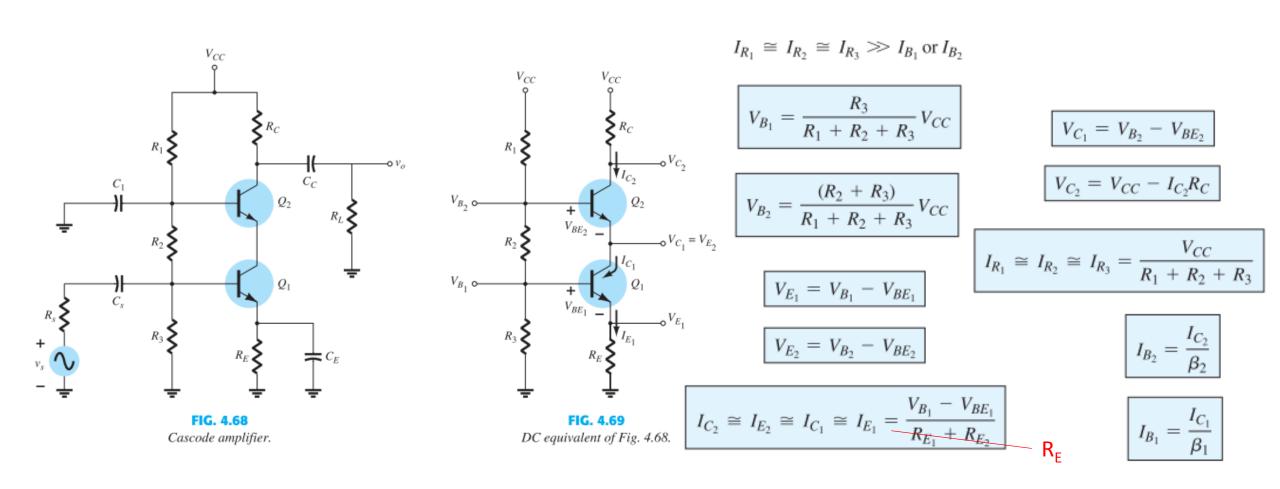
$$V_{C_2} = V_{CC}$$

$$V_{CE_2} = V_{C_2} - V_{E_2}$$

$$V_{CE_2} = V_{CC} - V_{E_2}$$

### Multiple BJT Networks (3 of 6)

Cascode configuration



### Multiple BJT Networks (4 of 6)

#### Feedback Pair

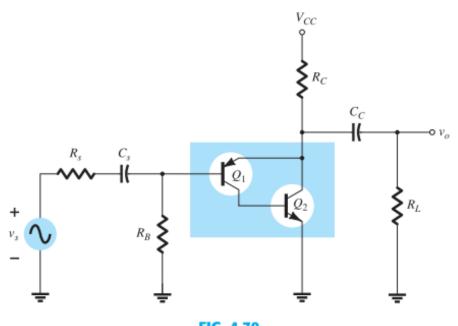


FIG. 4.70
Feedback Pair amplifier.

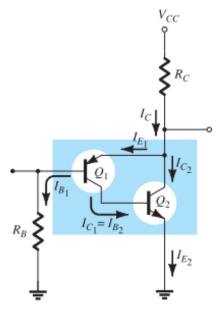


FIG. 4.71

DC equivalent of Fig. 4.70.

$$I_{B_2} = I_{C_1} = \beta_1 I_{B_1}$$
 $I_{C_2} = \beta_2 I_{B_2}$ 
 $I_{C_2} \cong I_{E_2} = \beta_1 \beta_2 I_{B_1}$ 

$$I_C = I_{E_1} + I_{E_2}$$
  
 $\cong \beta_1 I_{B_1} + \beta_1 \beta_2 I_{B_1}$   
 $= \beta_1 (1 + \beta_2) I_{B_1}$ 

$$I_C \cong \beta_1 \beta_2 I_{B_1}$$

$$V_{CC} - I_C R_C - V_{EB_1} - I_{B_1} R_B = 0$$
  
$$V_{CC} - V_{EB_1} - \beta_1 \beta_2 I_{B_1} R_C - I_{B_1} R_B = 0$$

$$I_{B_1} = \frac{V_{CC} - V_{EB_1}}{R_B + \beta_1 \beta_2 R_C}$$

$$V_{B_1} = I_{B_1} R_B$$

$$V_{B_2} = V_{BE_2}$$

$$V_{C_2} = V_{CC} - I_C R_C$$

$$V_{C_1} = V_{BE_2}$$

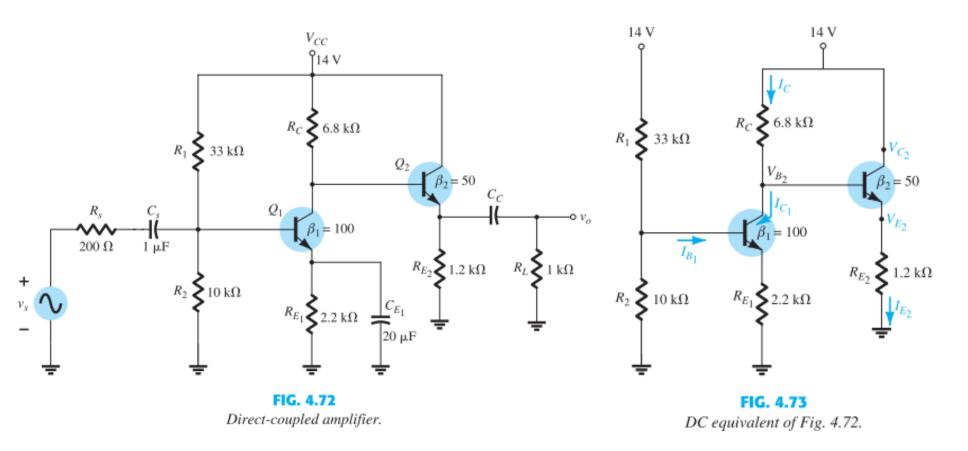
$$V_{CE_2} = V_{C_2}$$

$$V_{EC_1} = V_{E_1} - V_{C_1}$$

$$V_{EC_1} = V_{C_2} - V_{BE_2}$$

### Multiple BJT Networks (5 of 6)

Direct Coupled



$$I_{B_1} = rac{E_{ ext{Th}} - V_{BE}}{R_{ ext{Th}} + (eta + 1)R_{E_1}}$$
 $R_{ ext{Th}} = R_1 \| R_2$ 
 $E_{ ext{Th}} = rac{R_2 V_{CC}}{R_1 + R_2}$ 
 $V_{B_2} = V_{CC} - I_C R_C$ 
 $V_{E_2} = V_{B_2} - V_{BE_2}$ 
 $I_{E_2} = rac{V_{E_2}}{R_{E_2}}$ 
 $V_{CE_2} = V_{CC} - V_{E_2}$ 
 $V_{CE_2} = V_{CC} - V_{E_2}$ 

### Multiple BJT Networks (6 of 6)

 $V_E$ , =  $V_B$ , -  $V_{BE_2}$ and In this case,  $R_{\text{Th}} = 33 \text{ k}\Omega \parallel 10 \text{ k}\Omega = 7.67 \text{ k}\Omega$ resulting in and  $I_{B_1} = \frac{3.26 \text{ V} - 0.7 \text{ V}}{7.67 \text{ k}\Omega + (100 + 1) 2.2 \text{ k}\Omega}$ so that  $=\frac{2.56 \text{ V}}{}$  $= 11.17 \,\mu\text{A}$ Obviously, with  $I_{C_1} = \beta I_{B_1}$  $= 100 (11.17 \,\mu\text{A})$ 

and

In Fig. 4.73 we find that

$$V_{B_2} = V_{CC} - I_C R_C$$
  
= 14 V - (1.12 mA)(6.8 k $\Omega$ )  
= 14 V - 7.62 V  
= 6.38 V

 $= 1.12 \, \text{mA}$ 

 $V_{C_2} = V_{CC}$  $= 14 \, V$  $V_{CE_2} = V_{C_2} - V_{E_2}$  $V_{CE_2} = V_{CC} - V_{E_2}$ = 14 V - 5.68 V= 8.32 V

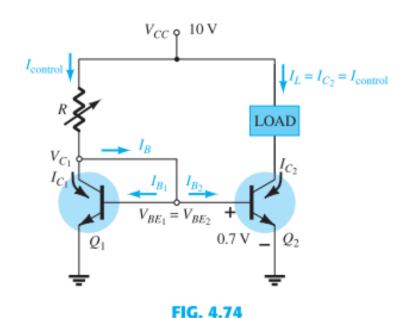
 $= \frac{5.68 \text{ V}}{1.2 \text{ k}\Omega}$ 

 $= 4.73 \, \text{mA}$ 

= 6.38 V - 0.7 V

 $= 5.68 \, V$ 

### Current Mirrors (1 of 2)



Current mirror using back-to-back BJTs.

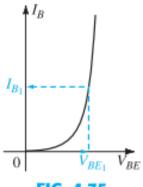


FIG. 4.75

Base characteristics
for transistor Q<sub>1</sub>
(and Q<sub>2</sub>).

$$I_{\text{control}} = \frac{V_{CC} - V_{BE}}{R}$$

$$I_{\text{control}} = I_{C_1} + I_B = I_{C_1} + 2I_{B_1}$$
  
 $I_{C_1} = \beta_1 I_{B_1}$   
 $I_{\text{control}} = \beta_1 I_{B_1} + 2I_{B_1} = (\beta_1 + 2)I_{B_1}$ 

 $\beta_1$  is typically  $\gg 2$ ,  $I_{\text{control}} \cong \beta_1 I_{B_1}$ 

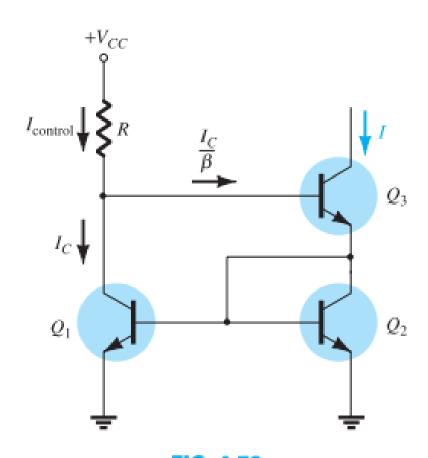
$$I_{B_1} = \frac{I_{\text{control}}}{\beta_1}$$

$$I_L = I_{C_2} = \beta \tilde{I}_{B_2}$$

$$I_{L} \uparrow I_{C_{2}} \uparrow I_{B_{2}} \uparrow V_{BE_{2}} \uparrow V_{CE_{1}} \uparrow, I_{R} \downarrow, I_{B} \downarrow, I_{B_{2}} \downarrow I_{C_{2}} \downarrow I_{L} \downarrow$$

$$-Note$$

### Current Mirrors (2 of 2)



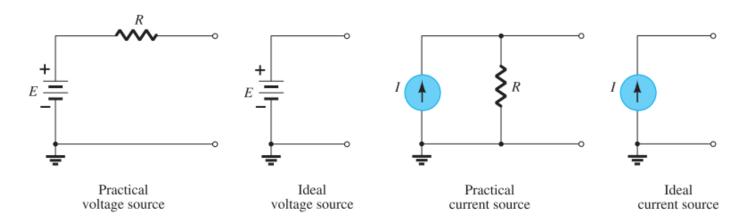
$$I_{\text{control}} = \frac{V_{CC} - 2V_{BE}}{R} \approx I_C + \frac{I_C}{\beta} = \frac{\beta + 1}{\beta}I_C \approx I_C$$

$$I \approx I_C = I_{\text{control}}$$

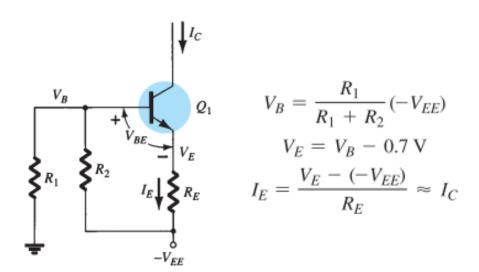
FIG. 4.78

Current mirror circuit with higher output impedance.

### Current Source Circuits (1 of 2)



#### **Bipolar Transistor Constant-Current Source**



#### FIG. 4.81

#### **Transistor/Zener Constant-Current Source**

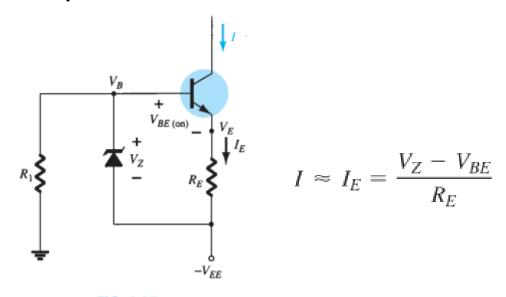


FIG. 4.83

Constant-current circuit using Zener diode.

### Current Source Circuits (2 of 2)

**EXAMPLE 4.30** Calculate the constant current *I* in the circuit of Fig. 4.84.

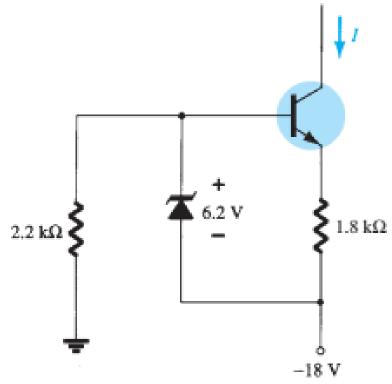


FIG. 4.84

Constant-current circuit for Example 4.30.

#### Solution:

Eq. (4.83): 
$$I = \frac{V_Z - V_{BE}}{R_E} = \frac{6.2 \text{ V} - 0.7 \text{ V}}{1.8 \text{ k}\Omega} = 3.06 \text{ mA} \approx 3 \text{ mA}$$

### PNP Transistors (1 of 2)

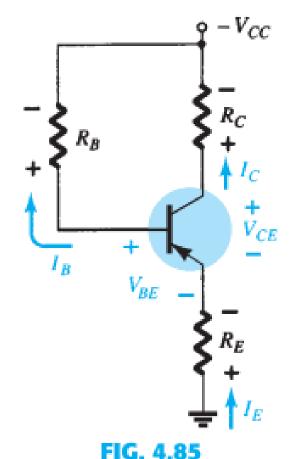
- The analysis thus far has been limited totally to npn transistors.
- Fortunately, the analysis of pnp transistors follows the same pattern established for npn transistors.
- In fact, the only difference between the resulting equations for a network in which an *npn* transistor has been replaced by a *pnp* transistor is the sign associated with particular quantities.

$$-I_E R_E + V_{BE} - I_B R_B + V_{CC} = 0$$

$$I_B = \frac{V_{CC} + V_{BE}}{R_B + (\beta + 1)R_E}$$

$$-I_E R_E + V_{CE} - I_C R_C + V_{CC} = 0$$

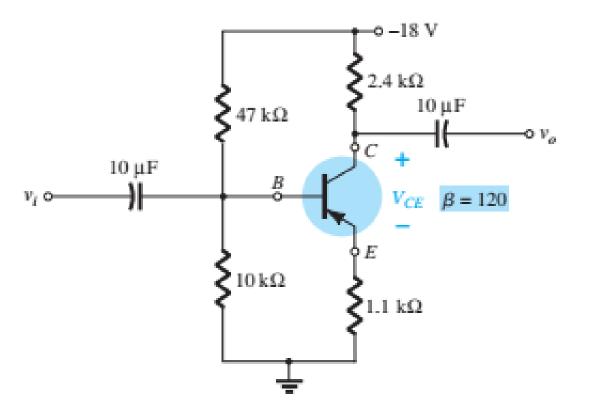
$$V_{CE} = -V_{CC} + I_C(R_C + R_E)$$



pnp transistor in an emitterstabilized configuration.

### PNP Transistors (2 of 2)

**EXAMPLE 4.31** Determine  $V_{CE}$  for the voltage-divider bias configuration of Fig. 4.86.

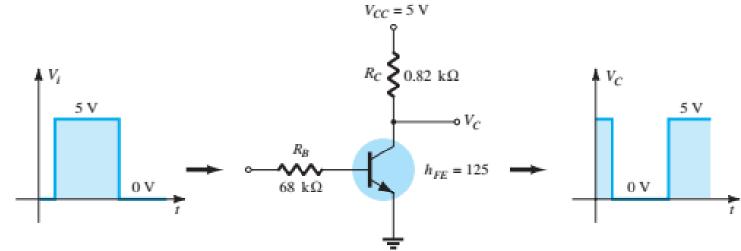


Write the equations of solution

FIG. 4.86

pnp transistor in a voltage-divider bias configuration.

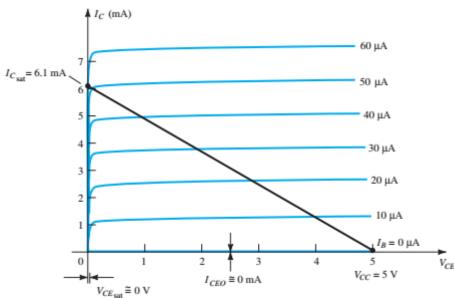
# Transistor Switching Networks (1 of 3)



$$I_{C_{\rm sat}} = \frac{V_{CC}}{R_C}$$

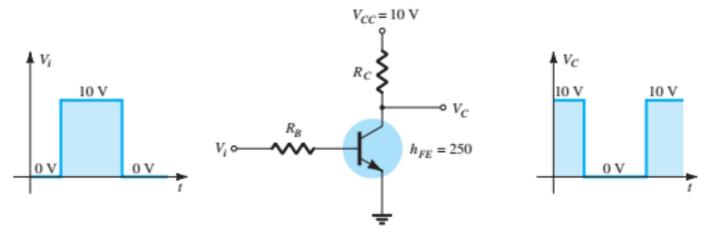
$$I_{B_{\max}} \cong \frac{I_{C_{\text{sat}}}}{\beta_{\text{dc}}}$$

$$I_B > rac{I_{C_{
m sat}}}{eta_{
m dc}}$$



## Transistor Switching Networks (2 of 3)

**EXAMPLE 4.32** Determine  $R_B$  and  $R_C$  for the transistor inverter of Fig. 4.90 if  $I_{C_{cot}} = 10 \text{ mA}$ .



**Solution:** At saturation,

$$I_{C_{\text{sat}}} = \frac{V_{CC}}{R_C}$$

and

$$10 \text{ mA} = \frac{10 \text{ V}}{R_C}$$

so that

$$R_C = \frac{10 \text{ V}}{10 \text{ mA}} = 1 \text{ k}\Omega$$

At saturation,

$$I_B \cong \frac{I_{C_{\text{sat}}}}{\beta_{\text{dc}}} = \frac{10 \text{ mA}}{250} = 40 \,\mu\text{A}$$

Choosing  $I_B = 60 \,\mu\text{A}$  to ensure saturation and using

$$I_B = \frac{V_i - 0.7 \text{ V}}{R_B}$$

we obtain

$$R_B = \frac{V_i - 0.7 \text{ V}}{I_B} = \frac{10 \text{ V} - 0.7 \text{ V}}{60 \mu \text{A}} = 155 \text{ k}\Omega$$

Choose  $R_B = 150 \text{ k}\Omega$ , which is a standard value. Then

$$I_B = \frac{V_i - 0.7 \text{ V}}{R_B} = \frac{10 \text{ V} - 0.7 \text{ V}}{150 \text{ k}\Omega} = 62 \,\mu\text{A}$$

and

$$I_B = 62 \,\mu\text{A} > \frac{I_{C_{\text{sat}}}}{\beta_{\text{dc}}} = 40 \,\mu\text{A}$$

Therefore, use  $R_B = 150 \text{ k}\Omega$  and  $R_C = 1 \text{ k}\Omega$ .

### Transistor Switching Networks (3 of 3)

$$R_{\rm sat} = \frac{V_{CE_{\rm sat}}}{I_{C_{\rm sat}}}$$

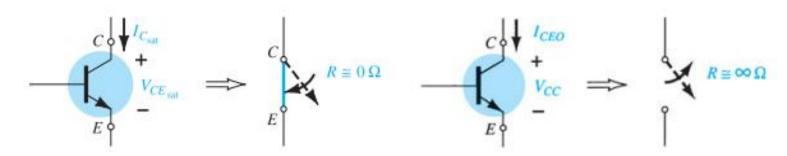


FIG. 4.88

Saturation conditions and the resulting terminal resistance.

FIG. 4.89

Cutoff conditions and the resulting terminal resistance.

Using a typical average value of  $V_{CE_{sat}}$  such as 0.15 V gives

$$R_{\rm sat} = \frac{V_{CE_{\rm sat}}}{I_{C_{\rm sat}}} = \frac{0.15 \text{ V}}{6.1 \text{ mA}} = 24.6 \Omega$$

$$R_{\text{cutoff}} = \frac{V_{CC}}{I_{CEO}} = \frac{5 \text{ V}}{0 \text{ mA}} = \infty \Omega$$

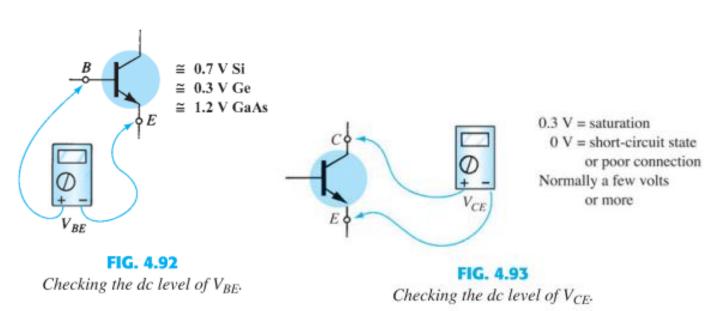
$$R_{\text{cutoff}} = \frac{V_{CC}}{I_{CEO}} = \frac{5 \text{ V}}{10 \,\mu\text{A}} = 500 \,\text{k}\Omega$$

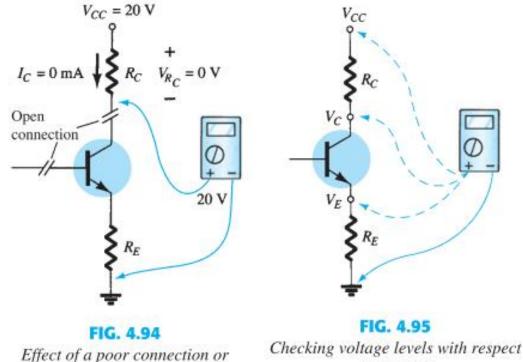
### Troubleshooting Techniques

• For an "on" transistor, the voltage  $V_{BE}$  should be in the neighborhood of 0.7 V.

• For the typical transistor amplifier in the active region,  $V_{CE}$  is usually about 25%

to 75% of  $V_{cc}$ .





damaged device.

to ground.

